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MICS:0060
00-0731

September 3, 2003

Ms. Stacy Summers
Micron Technology, Inc.
8000 South Federal Way
Boise, Idaho 83707-0006

Re: *U.S. Patent Application of David A. Zimlich*
Entitled: "Built-in Self Repair For An Integrated Circuit"

Dear Stacy:

Please find enclosed a copy of the Transmittal of Issue Fee and the Transmittal of Formal Drawings that we filed today, September 3, 2003, with the United States Patent and Trademark Office for the above-referenced application.

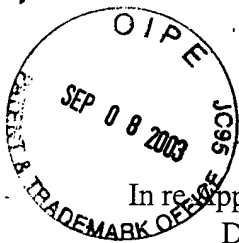
If you have any questions or comments, please do not hesitate to call.

Very truly yours,

Michael G. Fletcher

MGF/jp
enclosure

c: Michael L. Lynch, Esq. (w/o encl.)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
David Zimlich

Serial No.: 10/025,395

Filed: December 19, 2001

For: BUILT-IN SELF REPAIR FOR
AN INTEGRATED CIRCUIT

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Group Art Unit: 2819

Examiner: Le, D.

Atty Docket: MICS:0060
00-0731


Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

CERTIFICATE OF TRANSMISSION OR MAILING
37 C.F.R. 1.8

I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office in accordance with 37 C.F.R. 1.6(d) or is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on the date below:

September 3, 2003

Date


Michael G. Fletcher

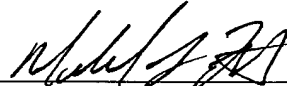
Sir:

TRANSMITTAL OF FORMAL DRAWINGS

Enclosed are four (4) sheets of formal drawings for the above-referenced patent application.

Respectfully submitted,

Date: September 3, 2003


Michael G. Fletcher

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